

Patent Abstracts of Japan

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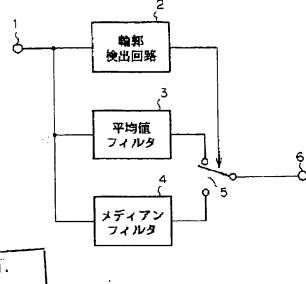
FUKUDA ATSUSHI;

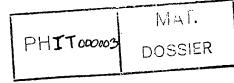
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TITLE

NOISE REDUCTION CIRCUIT





ABSTRACT :

PROBLEM TO BE SOLVED: To provide a noise reduction circuit which is capable of obtaining the large noise elimination effect without the blurring of an image and is equal to a frame cyclic type noise elimination system.

SOLUTION: The input image signals from an input terminal 1 are inputted in a contour detection circuit 2, an average value filter 3 and a median filter 4. By the signal from the contour detection circuit 2, the signal switching circuit 5 selects a filter, selects a median filter output at a contour part and selects an average value filter output at the parts except a counter. The median filter 4 exclusively eliminates impuse noise without blurring the contour of an image. The average value filter 3 has a large elimination effect for random noise. By combining these different filters, image quality degradation is suppressed and the noise elimination effect is increased.

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